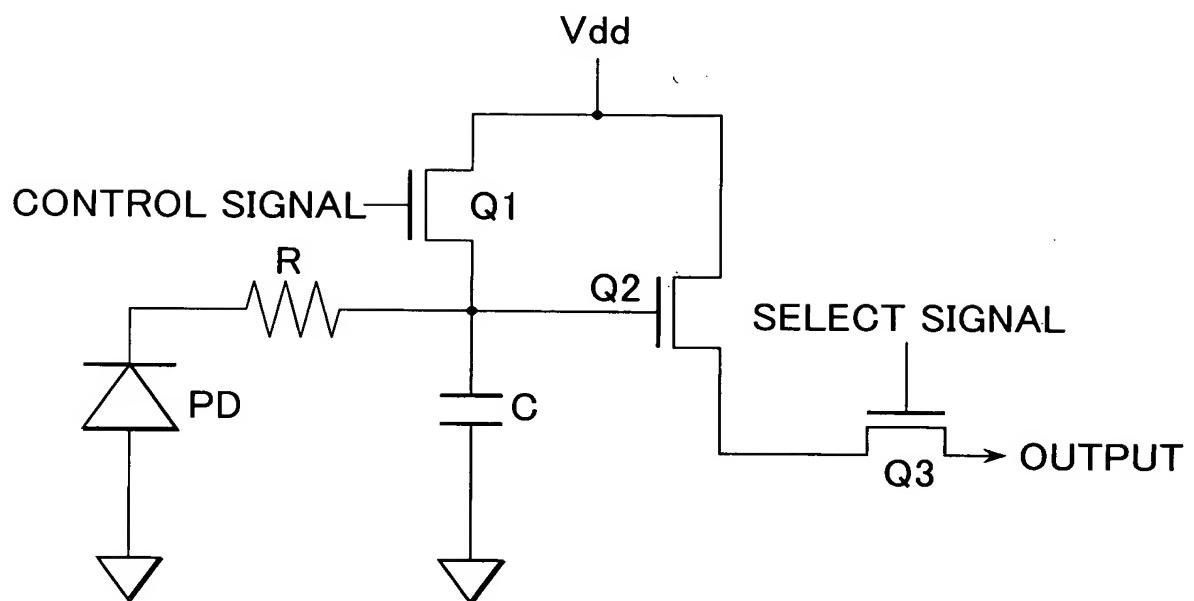


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FIG.1
(PRIOR ART)



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FIG.2A (PRIOR ART)

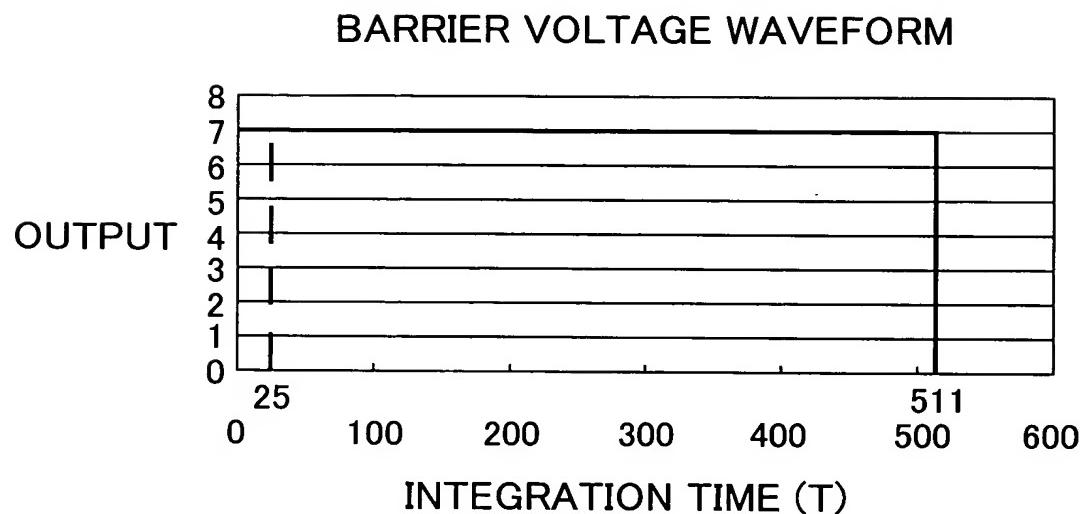
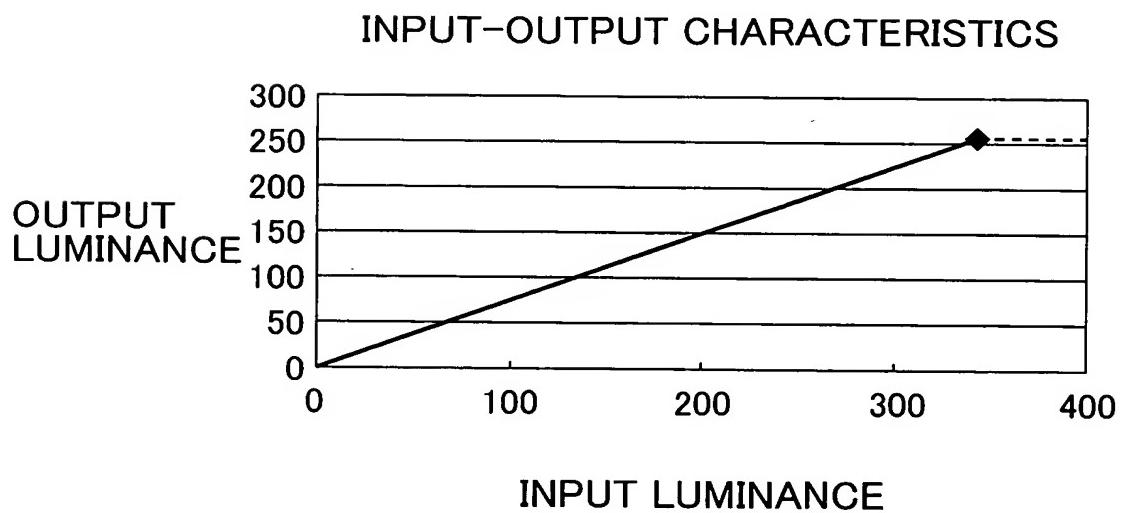


FIG.2B (PRIOR ART)



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FIG.3A (PRIOR ART)

BARRIER VOLTAGE WAVEFORM

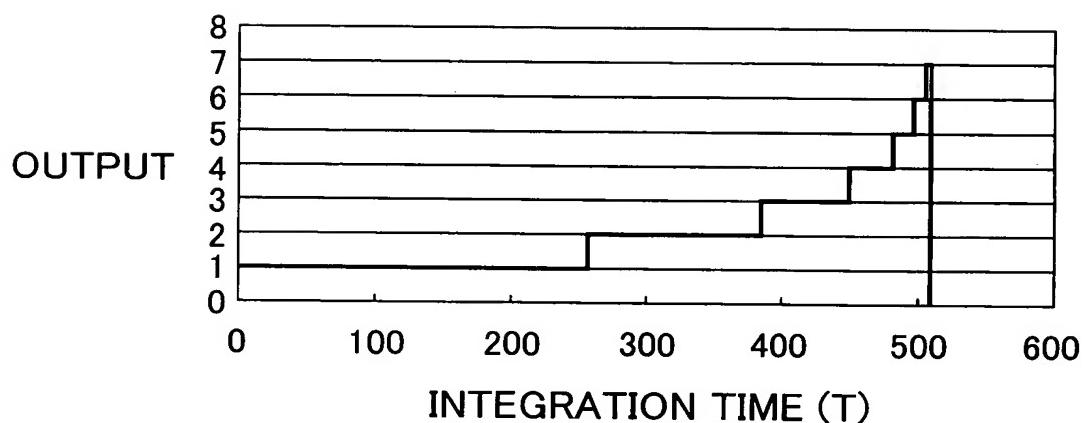
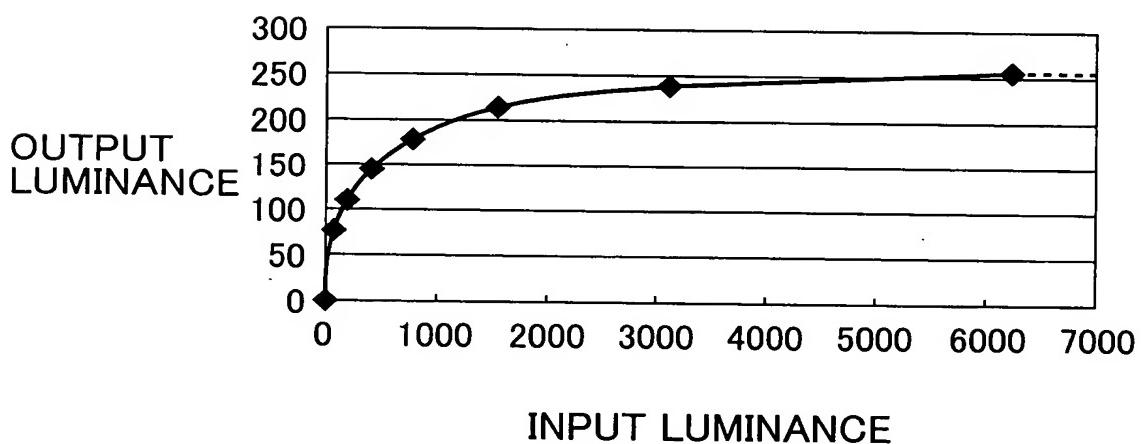


FIG.3B (PRIOR ART)

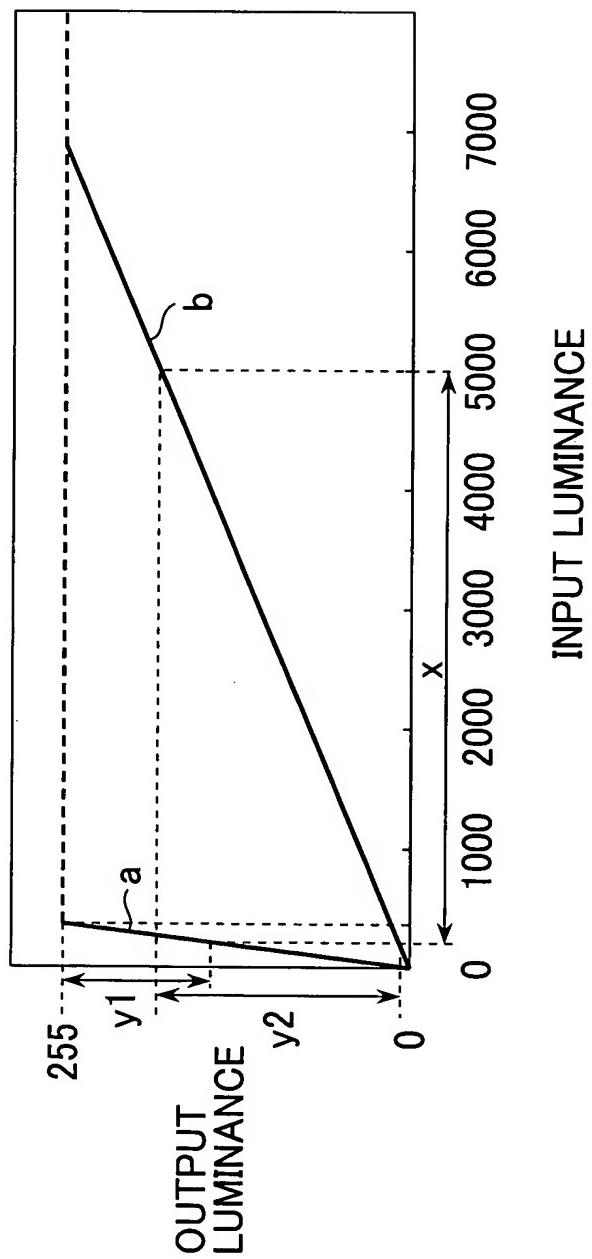
INPUT-OUTPUT CHARACTERISTICS



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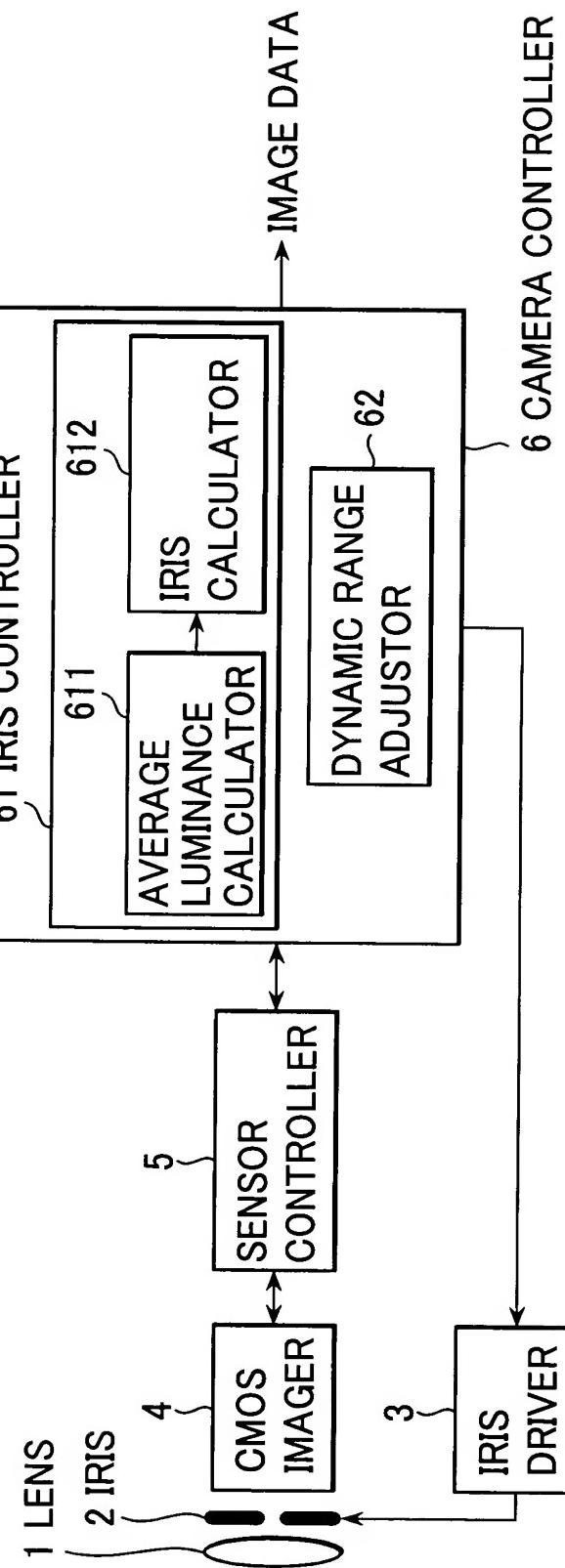
FIG.4
(PRIOR ART)

INPUT-OUTPUT CHARACTERISTICS



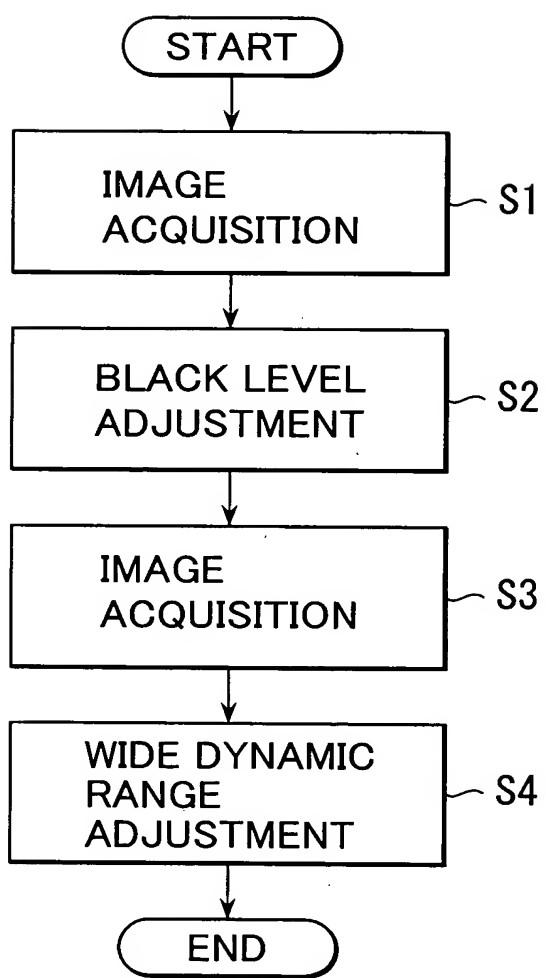
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FIG.5



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FIG.6



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FIG.7

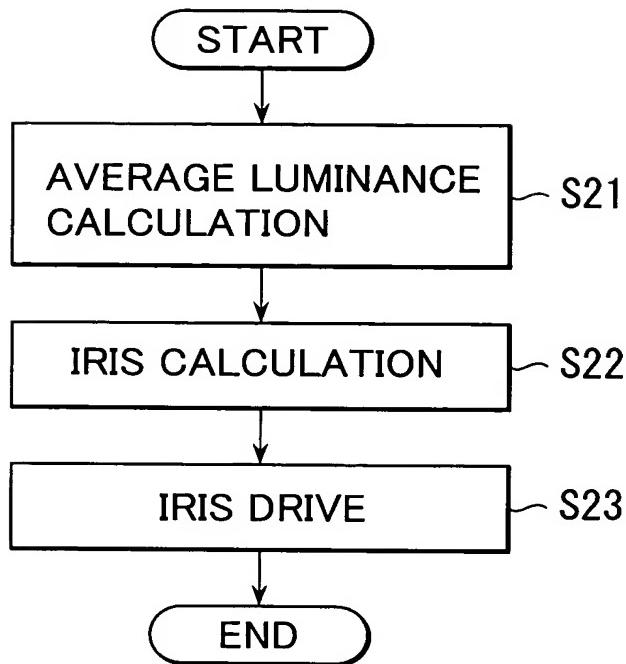
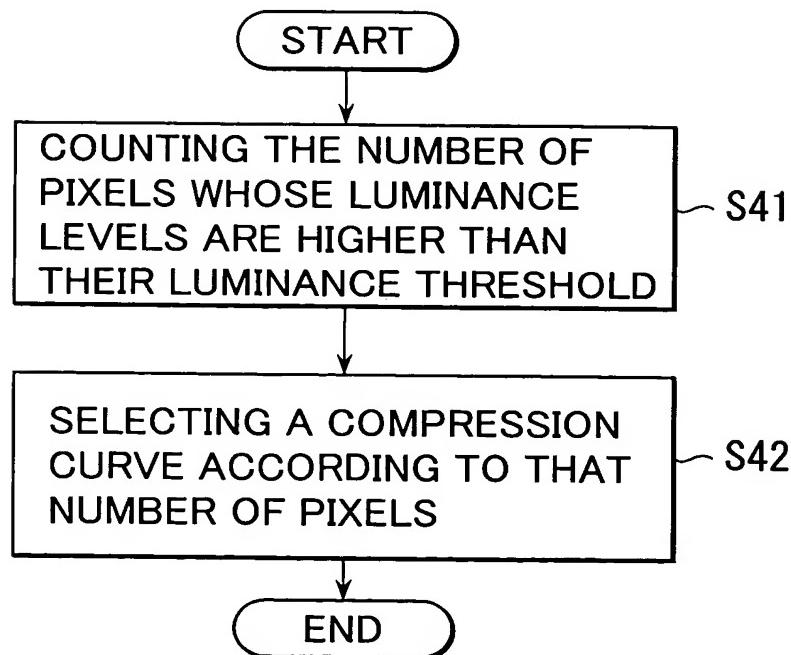


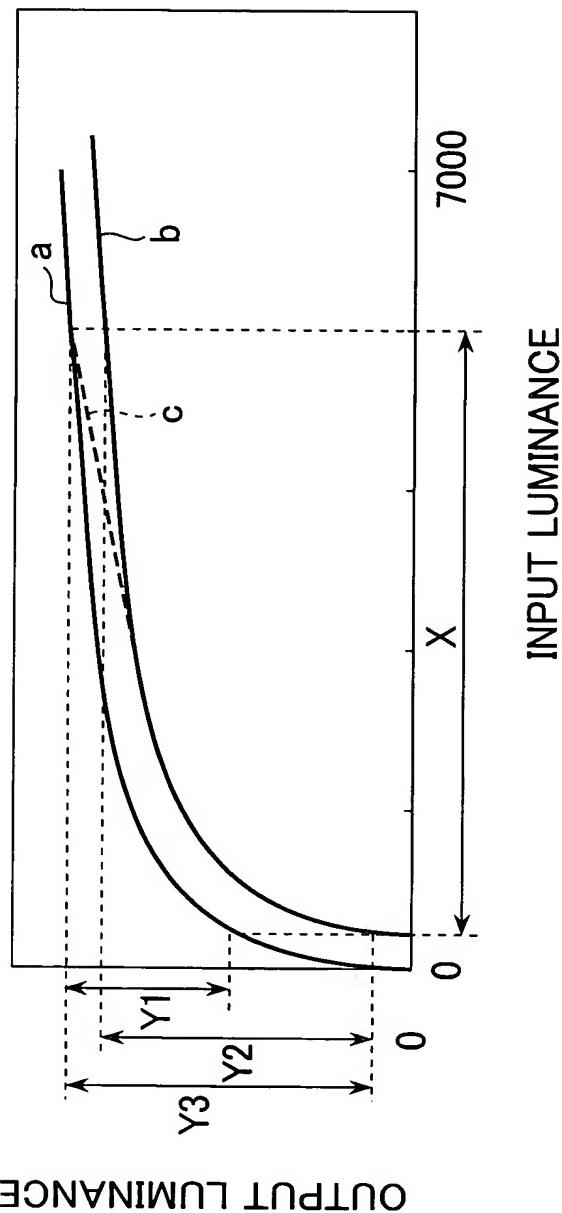
FIG.8



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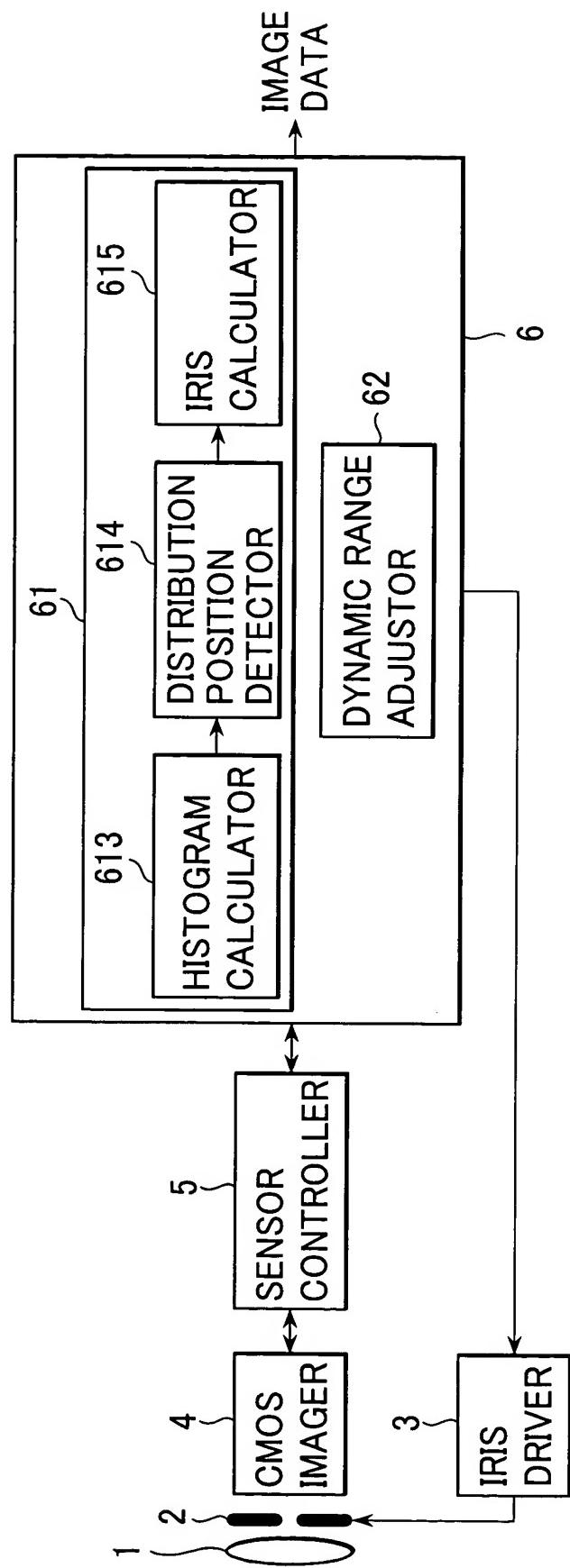
FIG.9

INPUT-OUTPUT CHARACTERISTICS



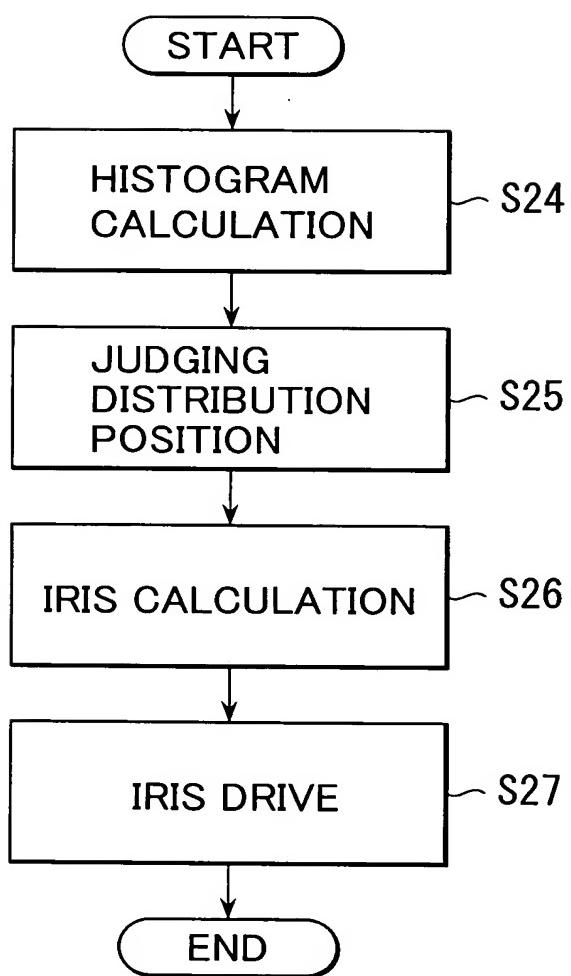
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FIG.10



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FIG.11



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FIG.12A

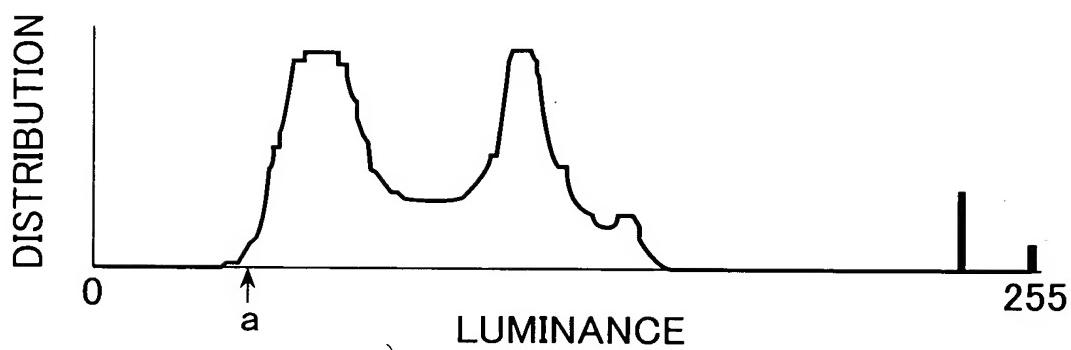


FIG.12B

